REMARKS

Claims 9-12 and 20-22 are presently pending. Claims 1-4 and 13-15 are withdrawn from consideration. Claims 5-8 and 16-19 are cancelled without prejudice. Claims 21 and 22 are added.

Claims 9 and 20 were rejected under 35 U.S.C. 103(a) as being obvious from the combination of Piazza in view of Luna. Claim 9 recites, among other limitations, "an extractor for transmitting an indicator to a direct memory access engine indicating that the portion of the compressed video data stored in the local buffer can be overwritten by another portion of the compressed video data, after the decompression engine decodes the portions of the video data stored in the local buffer". Claim 20 is amended to recite, among other limitations, "an extractor for transmitting a signal indicating that one of the portions of video data stored a portion of the local buffer can be overwritten with another portion of the compressed video data after the video decoder decodes the portions of compressed video data".

Examiner has indicated that "the present invention differs from Piazza et al. in that the present invention discloses an extractor for direct memory access engine control buffer, giving instructions that the compressed data stored in the buffer may be overwritten".

However, Examiner has also indicated that "VLD to DMA transfer unit DS1 31 to load data into buffer element MB_B1' (column 11: lines 9-23). Many processing steps are performed on the data stored in MB_B1', but the data remains as-is in MB_B1' (column 14: lines 25-30) until step S809G, when data for a new macroblock overwrite the data in MB_B1' (column 15: lines 19-21)."

Assignee respectfully traverses with respect to claim 9 and notes that in col. 11, line 16, DS1 31 transfers "decoded header of MB1" to MB_B1'. "In step S806F, DS1 31 transfers data decoded header for MB4 and DCT coefficients for MB3 from CM1 29 memory buffer, B0 to data cache 22's memory buffer, MB_B1'." Col. 13, Lines 11-13. However, "VLIW 21 also adds IDCTs and motion compensation data for MB1, and saves the added data in data cache 22, Output memory buffer, Output memory buffer, Out_B1'" during S806C that occurs *simultaneously* with steps S806F, among other steps. Col. 13, Line 17.

In contrast, claim 9 recites, "transmitting an indicator to a direct memory access engine indicating that the portion of the compressed video data stored in the local buffer can be overwritten by another portion of the compressed video data, *after* the decompression engine decodes the portions of the video data stored in the local buffer". Moreover, claim 22 recites, "wherein the extractor transmits the indicator *after the decompression engine finishes processing the portion*".

Accordingly, for at least the foregoing reasons, Assignee respectfully traverses the rejection to claims 9-12, and 20-22, and requests that Examiner withdraw them.

Conclusion

For at least the foregoing reasons, each of the pending claims is in a condition for allowance. Examiner is requested to pass this case to issuance.

Please charge any required fees not paid herewith or credit any overpayment to the Deposit Account of McAndrews, Held & Malloy, Ltd., Account No. 13-0017.

Respectfully submitted,

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